a

FIG. 2 is a detailed block diagram illustrating an ID Code register of the test access port of FIG. 1;

VERSION WITH MARKINGS TO SHOW CHANGES MADE

Please replace the paragraph beginning at page 4, line 20 with the following paragraph:

FIG. 2 is a detailed block diagram illustrating an [instruction] <u>ID Code</u> register of the test access port of FIG. 1;